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0001/PTO Rev. 10/95		U.S. Department of Commerce Patent and Trademark Office	
TRANSMITTAL FORM <i>(to be used for all correspondence during pendency of filed application)</i>		Application Number	10/763,020
		Filing Date	January 21, 2004
		First Named Inventor	Madian Somasundaram
		Group Art Unit Number	2818
		Examiner Name	Not yet known
Total Number of Pages in This Submission	5*	Attorney Docket Number	24084-08769

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REMARKS: *Page count does not include cited references.

SIGNATURE OF ATTORNEY OR AGENT			
Signature:			
Attorney/Reg. No.:	John T. McNelis, Reg. No. 37,186	Dated:	25 Sept 2004

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I hereby certify that this correspondence, including the enclosures identified above, is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on the date shown below. If the Express Mail Mailing Number is filled in below, then this correspondence is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service pursuant to 37 CFR 1.10.			
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT: Madian Somasundaram
APPLICATION NO.: 10/763,020
FILING DATE: January 21, 2004
TITLE: Circuits To Generate A Sequential Index For An Input Number In
A Pre-Defined List Of Numbers
EXAMINER: Not yet known
GROUP ART UNIT: 2818
ATTY. DKT. NO.: 24084-08769

CERTIFICATE OF MAILING

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John T. McNelis
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INFORMATION DISCLOSURE STATEMENT

Under 37 CFR §§ 1.56 and 1.97-98

SIR:

Pursuant to the provisions of 37 CFR §§ 1.56 and 1.97-98, enclosed herewith is modified form PTO/SB/08A listing references for consideration by the Examiner. Enclosed is a copy of each non-patent listed reference that may be material to the examination of this application, and for which there may be a duty to disclose.

The filing of this Information Disclosure Statement shall not be construed as a representation regarding the completeness of the list of references, or that inclusion of a reference in this list is an admission that it is prior art or is pertinent to this application, or that a search has been made, or as an admission that the information listed is, or may be considered to be, material to patentability, or that no other material information exists, and shall not be construed as an admission against interest in any manner.

This Information Disclosure Statement is being filed:

- ☒ within three months of the filing date of the application, or date of entry into the national stage of an international application, or before the mailing date of a first office action on the merits, whichever event last occurred;

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- ☐ before the mailing of a first official action after the filing of a request for continued examination (RCE) under 37 CFR § 1.114;
- ☐ after three months of the filing date of this national application or the date of entry of the national stage in an international application, or after the mailing date of the first official action on the merits, whichever event last occurred, but before the mailing date of the first to occur of either: (1) a final action under 37 CFR § 1.113; or (2) an action that otherwise closes prosecution in the application, and:
 - ☐ attached hereto is the fee set forth under 37 CFR § 1.17(p) for submission of this Information Disclosure Statement under 37 CFR § 1.97(c); OR
 - ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that:
 - ☐ each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; OR
 - ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application and, to the knowledge of the person signing this certification after making reasonable inquiry, no item of information contained in this Statement was known to any individual designated under 37 CFR § 1.56(c) more than three months prior to the filing of this Statement;
- ☐ on or before the payment of the issue fee but after the mailing date of the first to occur of either: (1) a final action under 37 CFR § 1.113; (2) a notice of allowance under 37 CFR § 1.311; or (3) an action that otherwise closes prosecution in the application, and:
 - ☐ Applicant certifies pursuant to 37 CFR § 1.97(e) that:
 - ☐ each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Statement; or
 - ☐ no item of information contained in this Information Disclosure Statement was cited in a communication from a

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foreign patent office in a counterpart foreign application and, to the knowledge of the person signing this certification after making reasonable inquiry, no item of information contained in this Statement was known to any individual designated under 37 CFR § 1.56(c) more than three months prior to the filing of this Statement; AND

- ☐ attached hereto is the fee set forth under 37 CFR §1.17(p) for submission of this Information Disclosure Statement under 37 CFR. § 1.97(c); OR
- ☐ after the payment of the issue fee. Applicant requests that the information contained in this Information Disclosure Statement be placed in the file according to 37 CFR § 1.97(i), although the information may not be considered by the USPTO.
- ☐ This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior application No. APPLICATION NO., filed on DATE FILED, and the references cited therein are hereby referenced, but are not required to be provided in this application under 37 CFR § 1.98(d).
- ☐ Each item of information contained in this Information Disclosure Statement was cited in a communication from a foreign patent office in a counterpart application, and the communication was not received by any individual designated in 37 CFR § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement. 37 CFR § 1.704(d).
- ☒ Applicant submits that no fee is required for the consideration of this Information Disclosure Statement.

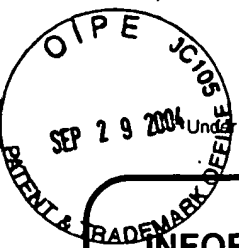
Consideration of the listed references and favorable action are solicited.

Respectfully submitted,
MADIAN SOMASUNDARAM

Dated: 25 Sept 2004

By: 

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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT****Complete if Known**

Application No.	10/763,020
Filing Date	January 21, 2004
First Named Inventor	Madian Somasundaram
Art Unit	2818
Examiner Name	Not yet known
Attorney Docket Number	24084-08769

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of

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U.S. PATENT DOCUMENTS

Examiner Initials*	Cite No. ¹	Document No. Number – Kind Code ² (if known)	Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document
		US-6,560,610	05-06-2003	Eatherton et al.
		US-6,331,961	12-18-2001	Kengeri et al.
		US-6,307,798	10-23-2001	Ahmed et al.
		US-6,266,262	07-24-2001	Washburn et al.

OTHER REFERENCES – NON-PATENT LITERATURE DOCUMENTS

Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ⁶
		BABOESCU, FLORIN and VARGHESE, GEORGE, Scalable Packet Classification, UC San Diego, SIGCOMM'01, August 27-31, 2001.	
		CAMs In Packet Classification: Can Fully Algorithmic Solutions Still Compete?, Netronics, July/August 2001.	
		commsdesign web pages [online]. CommsDesign.com [retrieved on 2004-02-02]. Retrieved from the Internet: <URL: http://www.commsdesign.com/printableArticle/?jsessionid=0WZT1VRSWGMPGQSNDBC... >, 1 page.	
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		GUPTA, PANKAJ et al., Routing Lookups in Hardware at Memory Access Speeds, Stanford University	
		GUPTA, PANKAJ and MCKEOWN, NICK, Packet Classification on Multiple Fields, Stanford University	
		ICHIRIU, MIKE, NetLogic Microsystems TM White Paper: High Performance Layer 3 Forwarding, 2000, pp. 1-6, NetLogic Microsystems, Inc.	
		MOSAID The Next Generation of Content Addressable Memories, September 1999, MOSAID Technologies Incorporated.	
		WELFELD, FELIKS, Packet Classification Processor: The Ultimate Branching Machine, CTO, Solidum, 2001.	
		UTCAM-Engine Technical Summary, UTC Microelectronic Systems, September 2000.	

Examiner Signature		Date Considered	
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*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609.

Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

¹Applicant's unique citation designation number (optional). ²See Kinds Codes of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. ³Enter Office that issued the document, by the two-letter code (WIPO Standard ST.3). ⁴For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. ⁵Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. ⁶Applicant is to place a check mark here if English language Translation is attached.